

100V Nch+Nch Power MOSFET

Datasheet

V _{DSS}	100V
R _{DS(on)} (Max.)	170mΩ
I _D	±3.0A
P _D	2.0W

Features

- 1) Low on resistance
- 2) Small Surface Mount Package (SOP8)
- 3) Pb-free plating ; RoHS compliant
- 4) Halogen free

Application

Switching Motor Drive

- 5) Sn100% plating
- 6) AEC-Q101 Qualified

Outline



Inner circuit



Packaging specifications

Packing	Embossed Tape
Reel size (mm)	330
Tape width (mm)	12
Quantity (pcs)	2500
Taping code	ТВ
Marking	SP8K52
	Reel size (mm) Tape width (mm) Quantity (pcs) Taping code

• Absolute maximum ratings ($T_a = 25^{\circ}C$, unless otherwise specified) <Tr1 and Tr2>

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Parameter	Symbol	Value	Unit
Drain - Source voltage	V _{DSS}	100	V
Continuous drain current	I _D	±3.0	А
Pulsed drain current	I _{DP} *1	±12	А
Gate - Source voltage	V _{GSS}	±20	V
	P _D *2	2.0	10/
Power dissipation (total)	P _D *3	1.4	W
Junction temperature	Tj	150	C°
Operating junction and storage temperature range	T _{stg}	-55 to +150	C°

•Thermal resistance

Deremeter	Symbol	Values			Unit
Parameter	Symbol	Min.	Тур.	Max.	Unit
Thermal registered junction embient (total)	R_{thJA}^{*2}	-	-	62.5	°C/W
Thermal resistance, junction - ambient (total)	R_{thJA}^{*3}	-	-	89.2	C/VV

•Electrical characteristics (T_a = 25°C) <Tr1 and Tr2>

Devenueter	Currence of	Conditions	Values			1.1	
Parameter	Symbol Conditions		Min.	Тур.	Max.	Unit	
Drain - Source breakdown voltage	V _{(BR)DSS}	$V_{(BR)DSS}$ $V_{GS} = 0V, I_D = 1mA$		-	-	V	
Breakdown voltage	$\Delta V_{(BR)DSS}$	I _D = 1mA		110.0		~~\//°C	
temperature coefficient	Δ T _j	referenced to 25°C	-	116.9	-	mV/°C	
Zero gate voltage drain current	I _{DSS}	I_{DSS} V_{DS} = 100V, V_{GS} = 0V		-	1	μA	
Gate - Source leakage current	I _{GSS}	I_{GSS} $V_{GS} = \pm 20V, V_{DS} = 0V$		-	±10	μA	
Gate threshold voltage	$V_{GS(th)}$ V_{DS} = 10V, I_D = 1mA		1.0	-	2.5	V	
Gate threshold voltage	$\Delta V_{GS(th)}$	I _D = 1mA		-3.6		m)//°C	
temperature coefficient	Δ T _j	referenced to 25°C	-	-3.0	-	mV/°C	
		V _{GS} = 10V, I _D = 3.0A	-	120	170		
Static drain - source on - state resistance	R _{DS(on)} *4	V _{GS} = 4.5V, I _D = 3.0A	-	130	180		
		V _{GS} = 4.0V, I _D = 3.0A	-	135	190		
Gate resistance	R _G	R_G f = 1MHz, open drain		6.9	-	Ω	
Forward Transfer Admittance	Y _{fs} *4	V _{DS} = 10V, I _D = 3.0A	3.5	-	-	S	

*1 Pw \leq 10µs, Duty cycle \leq 1%

- *2 Mounted on a ceramic board (30×30×0.8mm)
- *3 Mounted on a Cu board (40×40×0.8mm)

*4 Pulsed

•Electrical characteristics ($T_a = 25^{\circ}C$) <Tr1 and Tr2>

Deremeter	Sumpleal	Conditions		Unit			
Parameter	Symbol Conditions -		Min.	Тур.	Max.	Unit	
Input capacitance	C _{iss}	V _{GS} = 0V	-	610	-		
Output capacitance	C _{oss}	V _{DS} = 25V	-	55	-	pF	
Reverse transfer capacitance	C _{rss}	f = 1MHz	-	35	-		
Turn - on delay time	t _{d(on)} *4	$V_{DD} \simeq 50V, V_{GS}$ = 10V	-	13	-		
Rise time	t _r *4	I _D = 1.5A	-	13	-	20	
Turn - off delay time	$t_{d(off)}^{*4}$	R _L = 33Ω	-	50	-	ns	
Fall time	t _f *4	R _G = 10Ω	-	14	-		

•Gate charge characteristics ($T_a = 25^{\circ}C$) <Tr1 and Tr2>

Deremeter	Symbol Conditions		Values			1 1.0.14
Parameter	Symbol	Conditions	Min.	Тур.	Max.	Unit
Total gate charge	Qg ^{*4}		-	8.5	-	
Gate - Source charge	Q _{gs} *4	$V_{DD} \simeq 50$ V, $I_D = 3.0$ A $V_{GS} = 5$ V	-	1.8	-	nC
Gate - Drain charge	Q _{gd} ^{*4}		-	3.5	-	

•Body diode electrical characteristics (Source-Drain) ($T_a = 25^{\circ}C$)

<Tr1 and Tr2>

Parameter	Symbol	Conditions	Values			Unit	
	Conditions		Min.	Тур.	Max.	Unit	
Continuous forward current	۱ _s	T - 25°0	-	-	1.0	•	
Pulse forward current	I_{SP}^{*1}	T _a = 25°C	-	-	12	A	
Forward voltage	V_{SD}^{*4}	V _{GS} = 0V, I _S = 3.0A	-	-	1.2	V	





Fig.1 Power Dissipation Derating Curve

Fig.2 Maximum Safe Operating Area



Fig.3 Normalized Transient Thermal Resistance vs. Pulse Width

Fig.4 Single Pulse Maximum Power Dissipation









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Fig.5 Typical Output Characteristics(I)

V_{GS}= 10V

V_{GS}= 4.5V V_{GS}= 4.0V

0.1 0.2 0.3 0.4 0.5 0.6 0.7 0.8 0.9 1

Drain - Source Voltage : V_{DS} [V]

T_a=25°C

V_{GS}= 2.4V

V_{GS}= 2.2V

Pulsed

Fig.6 Typical Output Characteristics(II)



Fig.7 Breakdown Voltage vs. Junction Temperature







Fig.8 Typical Transfer Characteristics

Fig.9 Gate Threshold Voltage vs. Junction Temperature



Fig.10 Forward Transfer Admittance vs. Drain Current







Fig.11 Drain Current Derating Curve

Fig.12 Static Drain - Source On - State Resistance vs. Gate Source Voltage

Fig.13 Static Drain - Source On - State Resistance vs. Junction Temperature



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Fig.14 Static Drain - Source On - State

Fig.16 Static Drain - Source On - State

Resistance vs. Drain Current (III)

Fig.17 Static Drain - Source On - State Resistance vs. Drain Current (IV)

Fig.15 Static Drain - Source On - State

Resistance vs. Drain Current (II)



Resistance vs. Drain Current (I)





Fig.18 Typical Capacitances vs.

Fig.19 Switching Characteristics



Fig.20 Typical Gate Charge

Gate - Source Voltage : V_{GS} [V]



Fig.21 Source Current vs. Source Drain Voltage



•Measurement circuits <It is the same for the Tr1 and Tr2>





Fig.2-1 Gate Charge Measurement Circuit



Fig.1-2 Switching Waveforms











SP8K52HZG

Dimensions





b2

DIM	MILIM	ETERS	INC	HES
DIM	MIN	MAX	MIN	MAX
Α	-	1.75	-	0.069
A1	0.	15	0.0	006
A2	1.40	1.60	0.055	0.063
A3	0.3	25	0.0	010
b	0.30	0.50	0.012	0.020
с	0.10	0.30	0.004	0.012
D	4.80	5.20	0.189	0.205
E	3.75	4.05	0.148	0.159
е	1.3	1.27)50
HE	5.70	6.30	0.224	0.248
L1	0.40	0.60	0.016	0.024
Lp	0.65	0.85	0.026	0.033
х	0.	15	0.0	006
У	0.	10	0.004	

DIM		ETERS	INC	HES
DIM	MIN	MAX	MIN	MAX
b2	-	0.65	-	0.026
e1	5.	15	0.2	:03
1	-	1.15	, , ,	0.045

Dimension in mm/inches

v y s



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 - [e] Use of our Products in proximity to heat-producing components, plastic cords, or other flammable items
 - [f] Sealing or coating our Products with resin or other coating materials
 - [g] Use of our Products without cleaning residue of flux (Exclude cases where no-clean type fluxes is used. However, recommend sufficiently about the residue.); or Washing our Products by using water or water-soluble cleaning agents for cleaning residue after soldering
 - [h] Use of the Products in places subject to dew condensation
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- 5. Please verify and confirm characteristics of the final or mounted products in using the Products.
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- 7. De-rate Power Dissipation depending on ambient temperature. When used in sealed area, confirm that it is the use in the range that does not exceed the maximum junction temperature.
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 - [d] the Products are exposed to high Electrostatic
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