

CMOS LDO Regulators for Portable Equipments

1ch 500mA CMOS LDO Regulators



BUxxSA5WGWZ series

General Description

BUxxSA5WGWZ series are high-performance CMOS LDO regulators with output current ability of up to 500-mA.

These devices have excellent noise and load response characteristics despite of its low circuit current consumption of $33\mu A$. They are most appropriate for various applications such as power supplies for logic IC, RF, and camera modules.

Features

- High Output Voltage Accuracy: ±1.0%
- High Ripple Rejection: 70 dB (Typ, 1 kHz,)
- Compatible with small ceramic capacitor (Cin=Cout=0.47 µF)
- Low Current Consumption: 33µA
- Output Voltage ON/OFF control
- Built-in Over Current Protection Circuit (OCP)
- Built-in Thermal Shutdown Circuit (TSD)
- Adopting ultra-small WLCSP UCSP30L1

Applications

- Portable devices
- Camera modules
- Other electronic devices using microcontrollers or logic circuits

Key Specifications

■ Input Power Supply Voltage Range: 1.8V to 5.0V

Output Current Range: 0mA to 500mA

Operating Temperature Range: -40°C to +105°C

Output Voltage Lineup: 3.0V,3.3V

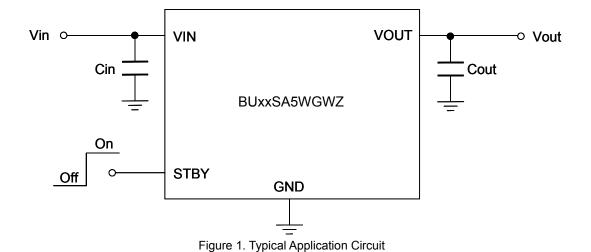
Output Voltage Accuracy: ±1.0%

Circuit Current: 33µA(Typ.)

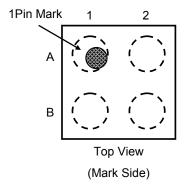
Standby Current: 0µA (Typ.)

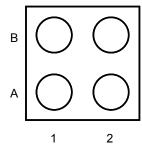
●Package W(Typ.) x D(Typ.) x H(Max.) UCSP30L1(BUXXSA5WGWZ) 0.8mm x 0.8mm x 0.33mm

●Typical Application Circuit



●Pin Configuration





Bottom View

●Pin Description

Pin No.	Symbol	Function
A1	GND	GND Pin
A2	STBY	Output Control Pin (High:ON, Low:OFF)
B1	VOUT	Output Pin
B2	VIN	Input Pin

Block Diagram

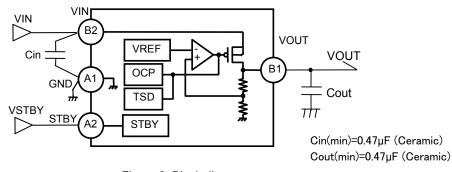


Figure 2. Block diagram

● Absolute Maximum Ratings

Parameter	Symbol	Rating	Unit
Power Supply Voltage	VMAX	-0.3 to +5.5	V
Power Dissipation	Pd	410 ^(*1)	mW
Maximum Junction Temperature	Tjmax	+125	°C
Operating Temperature Range	Topr	-40 to +105	°C
Storage Temperature Range	Tstg	-55 to +125	°C

^(*1) Derate by 4.1mW/°C when operating above Ta=25°C.(When mounted on a board 63mm × 55mm × 1.6mm glass-epoxy board, 9 layer)

Recommended Operating Ratings

Parameter	Symbol	Limit	Unit		
Input Power Supply Voltage Range	VIN	1.8 to 5.0	V		

Recommended Operating Conditions

Parameter	Symbol	Rating			Unit	Conditions	
Farameter	Symbol	Min.	Тур.	Max.	Offic	Conditions	
Input capacitor	Cin	0.47 ^(*2)	1.0	_	μF	A ceramic capacitor is recommended.	
Output capacitor	Cout	0.47 ^(*2)	1.0	_	μF	A ceramic capacitor is recommended.	

^(*2) Set the value of the capacitor so that it does not fall below the minimum value. Take into consideration the temperature characteristics, DC device characteristics, and degradation with time.

Electrical Characteristics

(Unless otherwise noted, Ta=-25°C, VIN=VouT+1.0V $^{(*6)}$, VSTBY=1.5V, Cin=1 μ F, Cout=1 μ F.)

PARAMETER		Symbol		Limit		Unit	Conditions	
TANAMETER		Syllibol	MIN.	TYP.	MAX.	Offic	Conditions	
Output Voltage 1		VOUT1	VOUT ×0.99	VOUT	VOUT ×1.01	V	IOUT = 10μA, VOUT≧2.5V	
Output Voltage 2		VOUT2	VOUT -25mV	VOUT	VOUT +25mV	٧	IOUT=10μA, VOUT < 2.5V	
Circuit Current		IGND	-	33	80	μA IOUT=0mA		
Circuit Current (S1	TBY)	BY) ICCST 1.0 µA STBY=0V		STBY=0V				
Ripple Rejection Ratio		RR	•	70	1	dB	VRR=-20dBv,fRR=1kHz,IOUT=10mA	
Dropout Voltage		VDROP	ı	80	150	mV	VIN=0.98×VOUT, IOUT=100mA Ta=-40 to +85°C ^(*4) , VOUT ≧ 2.5V	
Line Regulation		VDLI	-	2	8	mV	IOUT=10μA VIN=VOUT+0.5 to 5.0V ^(*5)	
Load Regulation1		VDLO1	-	6	45	mV	IOUT=0.01 to 300mA ^(*6)	
Load Regulation2		VDLO2	-	10	55	mV	IOUT=0.01 to 500mA ^(*6)	
STBY Pin Current		ISTBY	-	-	4.0	μΑ		
STBY Control	ON	VSTBH	1.2	-	VIN	V	Ta=-40 to +85℃	
Voltage	OFF	VSTBL	-0.2	-	0.3	V		

^(*3) VIN=3.5V for VOUT < 2.5V.

^(*4) Typical values apply for Ta=25°C. (*5) VIN=3.0V to 5.0V for VOUT<2.5V. (*6) Operating conditions are limited by Pd.

●Reference data BU33SA5WGWZ (Unless otherwise specified, Ta=25°C.)

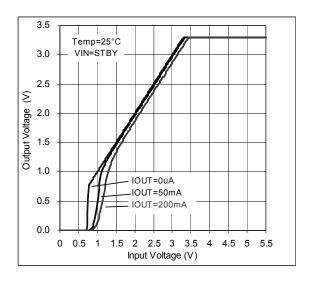


Figure 3. Output Voltage vs. Input Voltage

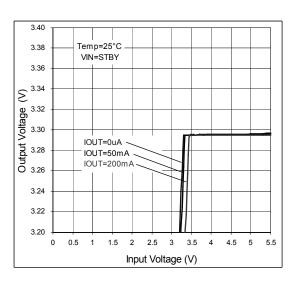


Figure 4. Line Regulation

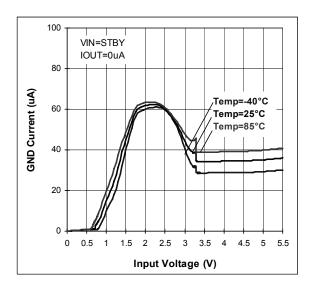


Figure 5. GND Current vs. Input Voltage

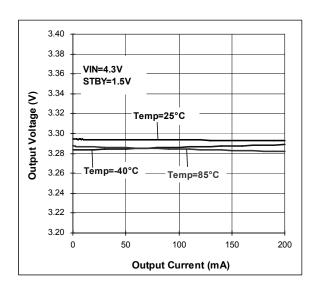


Figure 6. Load Regulation

● Reference data BU33SA5WGWZ (Unless otherwise specified, Ta=25°C.)

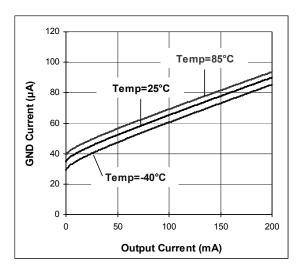


Figure 7. GND Current vs. Output Current

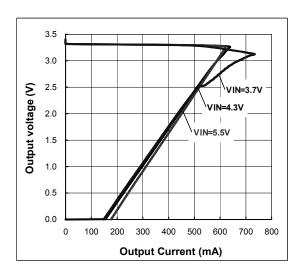


Figure 8. OCP Threshold

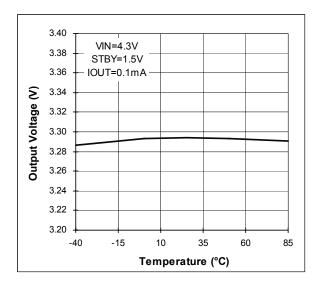


Figure 9. Output Voltage vs. Temperature

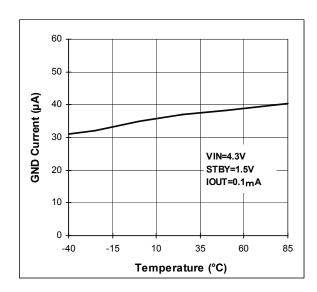


Figure 10. GND Current vs. Temperature

● Reference data BU33SA5WGWZ (Unless otherwise specified, Ta=25°C.)

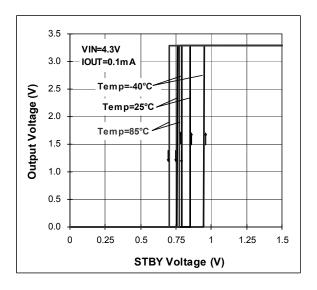


Figure 11. STBY Threshold

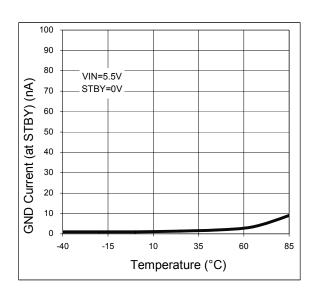


Figure 12. GND Current (at STBY) vs. Temperature

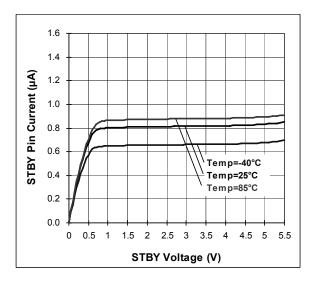


Figure 13. STBY Pin Current vs. STBY Voltage

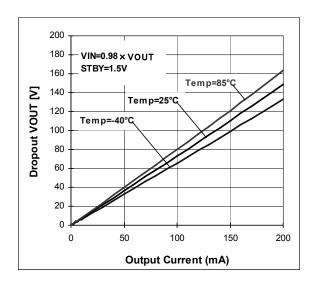


Figure 14. Dropout Voltage vs. Output Current

●Reference data BU33SA5WGWZ (Unless otherwise specified, Ta=25°C,Cin = Cout =1µF.)

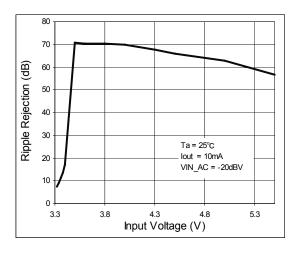


Figure 15. Ripple Rejection vs. Input Voltage

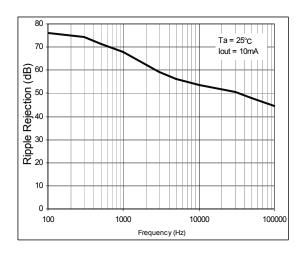


Figure 16. Ripple Rejection vs. Frequency

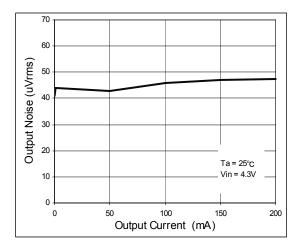


Figure 17. Output Noise vs. Output Current

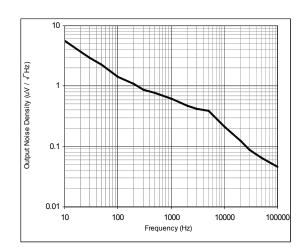
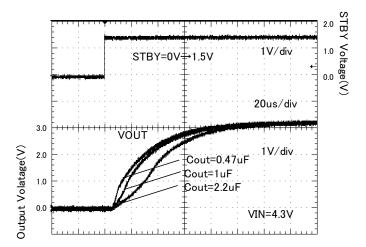


Figure 18. Output Noise Density vs. Frequency

●Reference data BU33SA5WGWZ (Unless otherwise specified, Ta=25°C,Cin = Cout =1µF.)



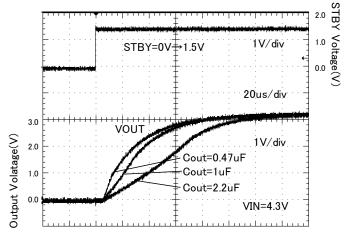


Figure 19. Startup time (Rout = none)

Figure 20. Startup time (Rout = 16.5 ohm)

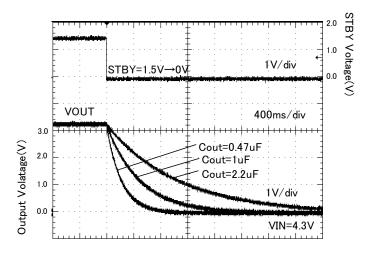


Figure 21. Discharge time (Rout = none)

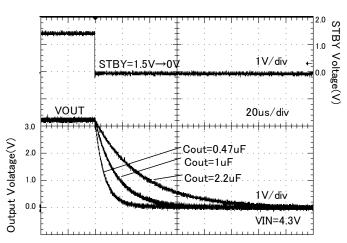
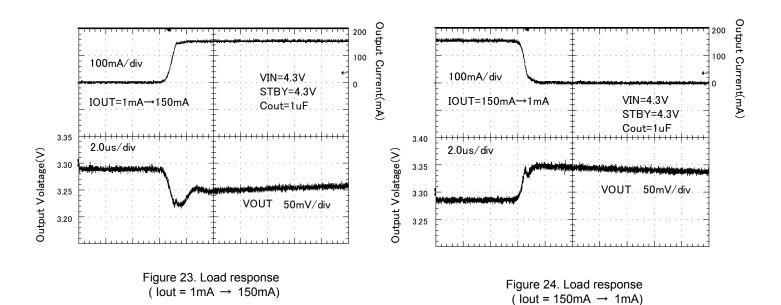
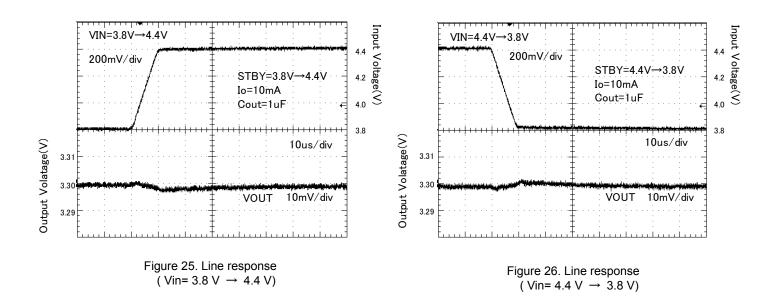


Figure 22. Discharge time (Rout = 16.5 ohm)

●Reference data BU33SA5WGWZ (Unless otherwise specified, Ta=25°C,Cin = Cout =1µF.)





●Input/Output Capacitor

It is recommended that an input capacitor is placed near pins between the VCC pin and GND as well as an output capacitor between the output pin and GND. The input is valid when the power supply impedance is high or when the PCB trace has significant length. For the output capacitor, the greater the capacitance, the more stable the output will be depending on the load and line voltage variations. However, please check the actual functionality of this capacitor by mounting it on a board for the actual application. Ceramic capacitors usually have different, thermal and equivalent series resistance characteristics, and may degrade gradually over continued use.

For additional details, please check with the manufacturer, and select the best ceramic capacitor for your application

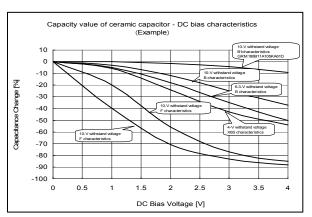


Figure 27. Capacity-bias characteristics

● Equivalent Series Resistance (ESR) of a Ceramic Capacitor

Capacitors generally have ESR (equivalent series resistance) and it operates stably in the ESR-IouT area shown on the right. Since ceramic capacitors, tantalum capacitors, electrolytic capacitors, etc. generally have different ESR, please check the ESR of the capacitor to be used and use it within the stability area range shown in the right graph for evaluation of the actual application.

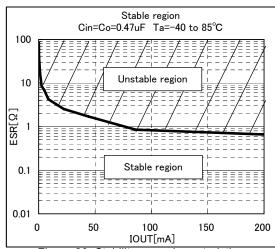


Figure 28. Stability area characteristics (Example)

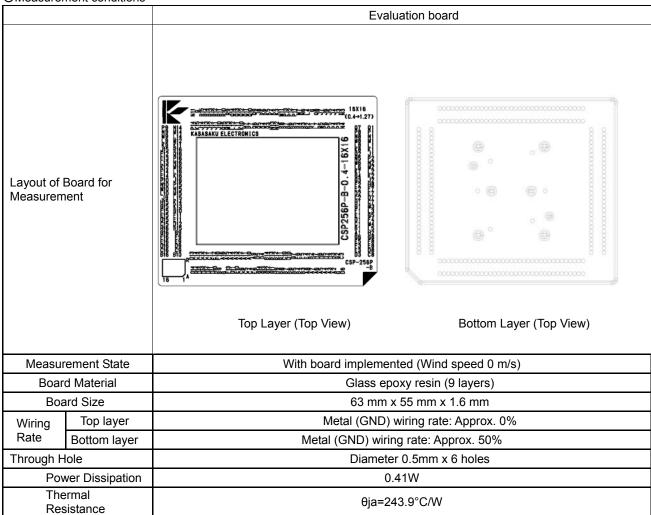
●Power Dissipation (Pd)

As for power dissipation, an estimate of heat reduction characteristics and internal power consumption of IC are shown, so please use these for reference. Since power dissipation changes substantially depending on the implementation conditions (board size, board thickness, metal wiring rate, number of layers and through holes, etc.), it is recommended to measure Pd on a set board. Exceeding the power dissipation of IC may lead to deterioration of the original IC performance, such as causing the operation of the thermal shutdown circuit or reduction in current capability. Therefore, be sure to prepare sufficient margin within power dissipation for usage.

Calculation of the maximum internal power consumption of IC (PMAX)

PMAX=(Vin-Vout)×Iomax Where: Vin=Input voltage Vout= Output voltage Iomax: Maximum output current)

OMeasurement conditions



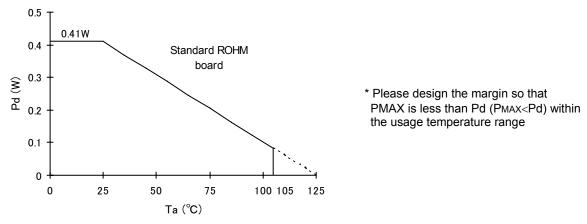


Figure 29. UCSP30L1(BUXXSA5WGWZ) Power dissipation heat reduction characteristics (Reference)

●I/O Equivalence Circuits

<u> </u>	-quivalence on cuits			
	B1 pin (VOUT)	A1 pin (GND)	A2 pin (STBY)	B2 pin (VIN)
	VIN O VOUT	<i></i>	STBY STBY	

Figure 30. Input / Output equivalent circuit

Operational Notes

1) Absolute maximum ratings

This produced with strict quality control, however it may be destroyed if operated beyond its absolute maximum ratings. In addition, it is impossible to predict all destructive situations such as short-circuit modes, open circuit modes, etc. Therefore, it is important to consider circuit protection measures, like adding a fuse, in case the IC is operated in a special mode exceeding the absolute maximum ratings.

2) GND Potential

GND potential must be the lowest potential of all pins of the IC at all operating conditions. Ensure that no pins are at a voltage below the ground pin at any time, even during transient condition.

3) Setting of Heat

Carry out the heat design that have adequate margin considering Pd of actual working states.

4) Pin Short and Mistake Fitting

When mounting the IC on the PCB, pay attention to the orientation of the IC. If there is mistake in the placement, the IC may be burned up.

5) Actions in Strong Magnetic Field

Using the IC within a strong magnetic field may cause the IC to malfunction.

6) Mutual Impedance

Use short and wide wiring tracks for the power supply and ground to keep the mutual impedance as small as possible. Use a capacitor to keep ripple to a minimum.

7) STBY Pin Voltage

To enable standby mode for all channels, set the STBY pin to 0.3V or less, and for normal operation, to 1.2V or more. Setting STBY to a voltage between 0.3 and 1.2V may cause malfunction and should be avoided. Keep transition time between high and low (or vice versa) to a minimum.

Additionally, if STBY is shorted to VIN, the IC will switch to standby mode and disable the output discharge circuit, causing a temporary voltage to remain on the output pin. If the IC is switched on again while this voltage is present, overshoot may occur on the output. Therefore, in applications where these pins are shorted, the output should always be completely discharged before turning the IC on.

8) Over Current Protection Circuit

Over current and short circuit protection is built-in at the output, and IC destruction is prevented at the time of load short circuit. These protection circuits are effective in the destructive prevention by sudden accidents, please avoid applications to where the over current protection circuit operates continuously.

9) Thermal Shutdown

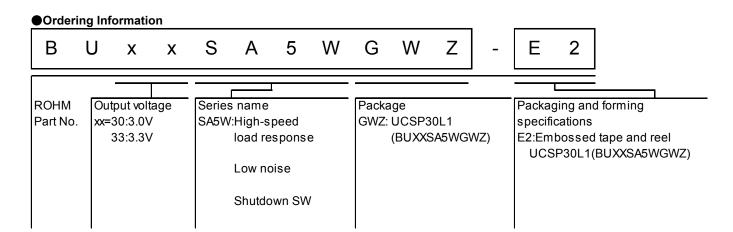
This IC has Thermal Shutdown Circuit (TSD Circuit). When the temperature of IC Chip is higher than 175°C, the output is turned off by TSD Circuit. TSD Circuit is only designed for protecting IC from thermal over load. Therefore it is not recommended that you design application where TSD will work in normal condition.

10) Actions under Strong light

A strong light like a halogen lamp may be caused malfunction. In our testing, fluorescence light and white LED causes little effects for the IC, but infrared light causes strong effects on the IC. The IC should be shielded from light like sunrays or halogen lamps.

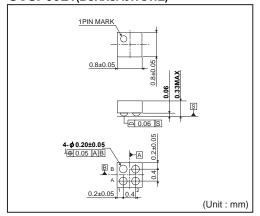
11) Output capacitor

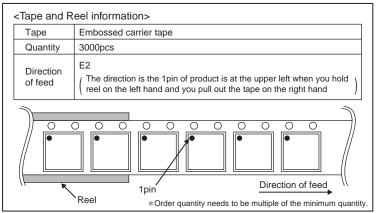
To prevent oscillation at output, it is recommended that the IC be operated at the stable region shown in Figure 28. It operates at the capacitance of more than $0.47\mu F$. As capacitance is larger, stability becomes more stable and characteristic of output load fluctuation is also improved.



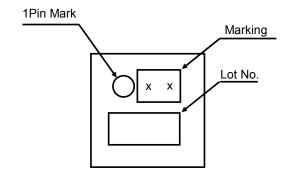
●Physical Dimension Tape and Reel Information

UCSP30L1(BUXXSA5WGWZ)





Marking Diagram



Part No.	Marking
BU30SA5WGWZ	DX
BU33SA5WGWZ	DV

Revision History

Date	Revision	Changes
09.May.2013	001	New Release

Notice

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JÁPAN	USA	EU	CHINA
CLASSⅢ	CL ACCIII	CLASS II b	CL ACCIII
CLASSIV	CLASSⅢ	CLASSⅢ	CLASSⅢ

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 - [b] Use of our Products outdoors or in places where the Products are exposed to direct sunlight or dust
 - [c] Use of our Products in places where the Products are exposed to sea wind or corrosive gases, including Cl₂, H₂S, NH₃, SO₂, and NO₂
 - [d] Use of our Products in places where the Products are exposed to static electricity or electromagnetic waves
 - [e] Use of our Products in proximity to heat-producing components, plastic cords, or other flammable items
 - [f] Sealing or coating our Products with resin or other coating materials
 - [g] Use of our Products without cleaning residue of flux (Exclude cases where no-clean type fluxes is used. However, recommend sufficiently about the residue.); or Washing our Products by using water or water-soluble cleaning agents for cleaning residue after soldering
 - [h] Use of the Products in places subject to dew condensation
- 4. The Products are not subject to radiation-proof design.
- 5. Please verify and confirm characteristics of the final or mounted products in using the Products.
- 6. In particular, if a transient load (a large amount of load applied in a short period of time, such as pulse. is applied, confirmation of performance characteristics after on-board mounting is strongly recommended. Avoid applying power exceeding normal rated power; exceeding the power rating under steady-state loading condition may negatively affect product performance and reliability.
- 7. De-rate Power Dissipation depending on ambient temperature. When used in sealed area, confirm that it is the use in the range that does not exceed the maximum junction temperature.
- 8. Confirm that operation temperature is within the specified range described in the product specification.
- 9. ROHM shall not be in any way responsible or liable for failure induced under deviant condition from what is defined in this document.

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- 2. In principle, the reflow soldering method must be used on a surface-mount products, the flow soldering method must be used on a through hole mount products. If the flow soldering method is preferred on a surface-mount products, please consult with the ROHM representative in advance.

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This Product is electrostatic sensitive product, which may be damaged due to electrostatic discharge. Please take proper caution in your manufacturing process and storage so that voltage exceeding the Products maximum rating will not be applied to Products. Please take special care under dry condition (e.g. Grounding of human body / equipment / solder iron, isolation from charged objects, setting of lonizer, friction prevention and temperature / humidity control).

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- 1. Product performance and soldered connections may deteriorate if the Products are stored in the places where:
 - [a] the Products are exposed to sea winds or corrosive gases, including Cl₂, H₂S, NH₃, SO₂, and NO₂
 - [b] the temperature or humidity exceeds those recommended by ROHM
 - [c] the Products are exposed to direct sunshine or condensation
 - [d] the Products are exposed to high Electrostatic
- Even under ROHM recommended storage condition, solderability of products out of recommended storage time period
 may be degraded. It is strongly recommended to confirm solderability before using Products of which storage time is
 exceeding the recommended storage time period.
- 3. Store / transport cartons in the correct direction, which is indicated on a carton with a symbol. Otherwise bent leads may occur due to excessive stress applied when dropping of a carton.
- 4. Use Products within the specified time after opening a humidity barrier bag. Baking is required before using Products of which storage time is exceeding the recommended storage time period.

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