

# Automotive-grade N-channel 60 V, 4.4 mΩ typ., 80 A STripFET™ VI DeepGATE™ Power MOSFET in a TO-220 package

Datasheet - production data

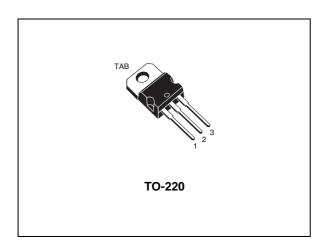
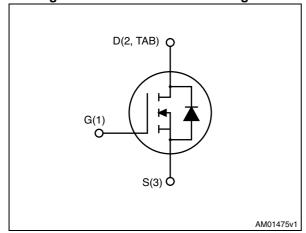


Figure 1. Internal schematic diagram



#### **Features**

Order code	V <sub>DS</sub>	R <sub>DS(on)</sub> max.	I <sub>D</sub>
STP80N6F6	60 V	5 mΩ	80 A <sup>(1)</sup>

- 1. Current limited by package
- Designed for automotive applications and AEC-Q101 qualified
- · Low gate charge
- · Very low on-resistance
- High avalanche ruggedness

#### **Applications**

Switching applications

#### **Description**

This device is an N-channel Power MOSFET developed using the  $6^{th}$  generation of STripFET<sup>TM</sup> DeepGATE<sup>TM</sup> technology, with a new gate structure. The resulting Power MOSFET exhibits the lowest  $R_{DS(on)}$  in all packages.

**Table 1. Device summary** 

Order code	Marking	Packages	Packaging
STP80N6F6	80N6F6	TO-220	Tube

Contents STP80N6F6

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STP80N6F6 Electrical ratings

# 1 Electrical ratings

Table 2. Absolute maximum ratings

Symbol	Parameter	Value	Unit
V <sub>DS</sub>	Drain-source voltage	60	V
V <sub>GS</sub>	Gate-source voltage	± 20	٧
I <sub>D</sub> <sup>(1)</sup>	Drain current (continuous) at T <sub>C</sub> = 25 °C	80	Α
I <sub>D</sub> <sup>(1)</sup>	Drain current (continuous) at T <sub>C</sub> = 100 °C	80	Α
I <sub>DM</sub> <sup>(1)</sup>	Drain current (pulsed)	320	Α
P <sub>TOT</sub>	Total dissipation at T <sub>C</sub> = 25 °C	120	W
	Derating factor	0.8	W/°C
T <sub>stg</sub>	Storage temperature		°C
T <sub>j</sub>	Operating junction temperature	- 55 to 175	

<sup>1.</sup> Current limited by package

Table 3. Thermal data

Symbol	Parameter	Value	Unit
R <sub>thj-case</sub>	Thermal resistance junction-case max	1.25	°C/W
R <sub>thj-a</sub>	Thermal resistance junction-ambient max	62.5	°C/W

Electrical characteristics STP80N6F6

### 2 Electrical characteristics

(T<sub>CASE</sub> = 25 °C unless otherwise specified)

Table 4. On/off states

Symbol	Parameter	Test conditions	Min.	Тур.	Max.	Unit
V <sub>(BR)DSS</sub>	Drain-source breakdown voltage (V <sub>GS</sub> = 0)	I <sub>D</sub> = 250 μA	60			V
1	I <sub>DSS</sub> Zero gate voltage Drain current (V <sub>GS</sub> = 0)	V <sub>DS</sub> = 60 V			1	μΑ
DSS		V <sub>DS</sub> = 60 V, T <sub>C</sub> =125 °C			100	μΑ
I <sub>GSS</sub>	Gate-body leakage current (V <sub>DS</sub> = 0)	V <sub>GS</sub> = ± 20 V			± 100	nA
V <sub>GS(th)</sub>	Gate threshold voltage	$V_{DS} = V_{GS}, I_{D} = 250 \mu\text{A}$	3		4.5	٧
R <sub>DS(on)</sub>	Static drain-source on-resistance	V <sub>GS</sub> = 10 V, I <sub>D</sub> = 40 A		4.4	5	mΩ

#### Table 5. Dynamic

Symbol	Parameter	Test conditions	Min.	Тур.	Max.	Unit
C <sub>iss</sub>	Input capacitance		-	8325	-	pF
C <sub>oss</sub>	Output capacitance	$V_{DS} = 25 \text{ V, f} = 1 \text{ MHz,}$ $V_{GS} = 0$	ı	500	ı	pF
C <sub>rss</sub>	Reverse transfer capacitance		-	400	-	pF
Qg	Total gate charge	V <sub>DD</sub> = 30 V, I <sub>D</sub> = 80 A, V <sub>GS</sub> = 10 V	-	147	-	nC
Q <sub>gs</sub>	Gate-source charge		-	44	-	nC
$Q_{gd}$	Gate-drain charge	1 GS 1 5 1	-	46	-	nC

#### Table 6. Switching times

		_				
Symbol	Parameter	Test conditions	Min.	Тур.	Max.	Unit
t <sub>d(on)</sub>	Turn-on delay time		-	40	-	ns
t <sub>r</sub>	Rise time	V <sub>DD</sub> = 30 V, I <sub>D</sub> = 40 A		71		ns
t <sub>d(off)</sub>	Turn-off-delay time	$R_{G} = 4.7 \Omega V_{GS} = 10 V$	-	132	-	ns
t <sub>f</sub>	Fall time		-	40	-	ns

Table 7. Source drain diode

Symbol	Parameter	Test conditions	Min.	Тур.	Max	Unit
I <sub>SD</sub>	Source-drain current		-		80	Α
I <sub>SDM</sub> <sup>(1)</sup>	Source-drain current (pulsed)		-		320	Α
V <sub>SD</sub> (2)	Forward on voltage	I <sub>SD</sub> = 80 A, V <sub>GS</sub> = 0	-		1.3	٧
t <sub>rr</sub>	Reverse recovery time	I <sub>SD</sub> = 80 A, V <sub>DD</sub> = 48 V	-	46		ns
Q <sub>rr</sub>	Reverse recovery charge	$di/dt = 100 A/\mu s$ ,	-	65		nC
I <sub>RRM</sub>	Reverse recovery current	T <sub>j</sub> = 150 °C	-	2.8		Α

<sup>1.</sup> Current limited by package.

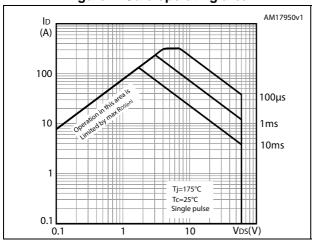
<sup>2.</sup> Pulsed: pulse duration = 300  $\mu$ s, duty cycle 1.5%

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#### 2.1 Electrical characteristics (curves)

Figure 2. Safe operating area

Figure 3. Thermal impedance



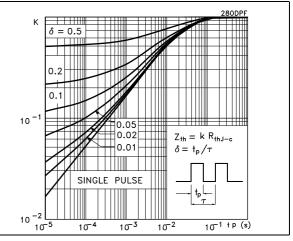
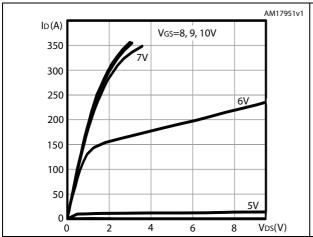


Figure 4. Output characteristics

Figure 5. Transfer characteristics



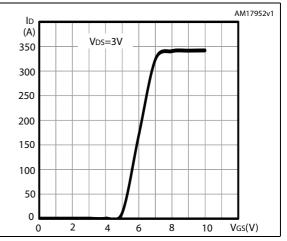
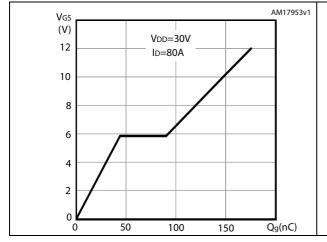
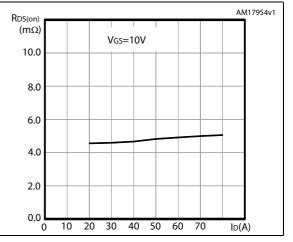


Figure 6. Gate charge vs gate-source voltage

Figure 7. Static drain-source on-resistance





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Figure 8. Capacitance variations

Figure 9. Normalized gate threshold voltage vs temperature

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TJ(°C)

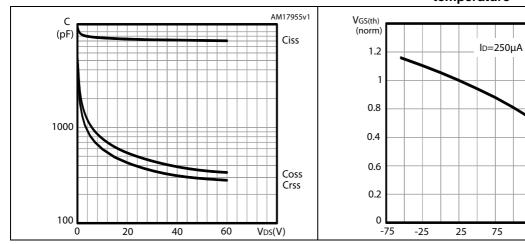


Figure 10. Normalized on-resistance vs temperature

Figure 11. Normalized  $\ensuremath{V_{DS}}$  vs temperature

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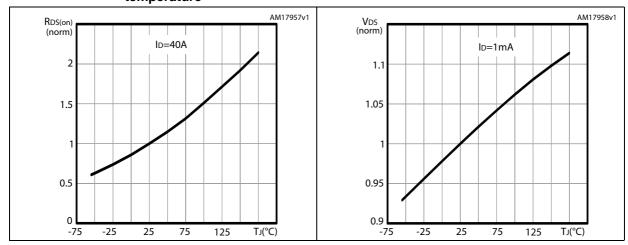
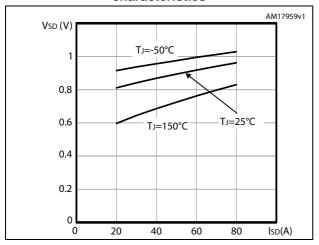


Figure 12. Source-drain diode forward characteristics



# 3 Package mechanical data

In order to meet environmental requirements, ST offers these devices in different grades of ECOPACK<sup>®</sup> packages, depending on their level of environmental compliance. ECOPACK<sup>®</sup> specifications, grade definitions and product status are available at: <a href="https://www.st.com">www.st.com</a>. ECOPACK<sup>®</sup> is an ST trademark.



øΡ Ε H1 D <u>D1</u> L20 L30 b1(X3) -- b (X3) \_e1\_\_\_ 0015988\_typeA\_Rev\_T

Figure 13. TO-220 type A drawing

Table 8. TO-220 type A mechanical data

Dim		mm	
Dim.	Min.	Тур.	Max.
А	4.40		4.60
b	0.61		0.88
b1	1.14		1.70
С	0.48		0.70
D	15.25		15.75
D1		1.27	
Е	10		10.40
е	2.40		2.70
e1	4.95		5.15
F	1.23		1.32
H1	6.20		6.60
J1	2.40		2.72
L	13		14
L1	3.50		3.93
L20		16.40	
L30		28.90	
ØP	3.75		3.85
Q	2.65		2.95

STP80N6F6 Revision history

# 4 Revision history

**Table 9. Document revision history** 

Date	Revision	Changes
08-Aug-2012	1	Initial release.
21-Jan-2014	2	<ul> <li>Document status promoted from preliminary to production data</li> <li>Modified: title</li> <li>Modified: Features</li> <li>Added: note 1 in cover page</li> <li>Modified: R<sub>DS(on)max</sub> and I<sub>D</sub> values in cover page</li> <li>Modified: I<sub>D</sub> (at TC = 25 °C and at TC = 100 °C) values, I<sub>D</sub>, I<sub>DM</sub> values and added note 1 in Table 2</li> <li>Modified: R<sub>thj-case</sub> value in Table 3</li> <li>Modified: R<sub>DS(on)</sub> values in Table 4</li> <li>Modified: I<sub>D</sub> and the entire typical values in Table 5, 6 and 7</li> <li>Added: Section 2.1: Electrical characteristics (curves)</li> <li>Updated: Section 3: Package mechanical data</li> <li>Minor text changes</li> </ul>

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