

### STPS15L30CDJF

# Low drop power Schottky rectifier

### **Features**

- Very small conduction losses
- Negligible switching losses
- Extremely fast switching
- Low forward voltage drop
- Low thermal resistance
- High avalanche capability specified

### **Description**

Dual center tap Schottky rectifier suited for switch mode power supply and high frequency DC to DC converters.

Packaged in PowerFLAT™, this device is intended for use in low voltage, high frequency inverters, free-wheeling and polarity protection applications.

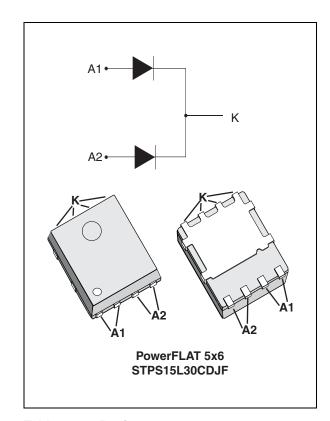


Table 1. Device summary

Symbol	Value
I <sub>F(AV)</sub>	2 x 7.5 A
$V_{RRM}$	30 V
T <sub>j</sub> (max)	150 °C
V <sub>F</sub> (typ)	0.34 V

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STPS15L30CDJF **Characteristics** 

#### **Characteristics** 1

Table 2. Absolute ratings (limiting values, per diode)

Symbol	Paramete	Value	Unit			
V <sub>RRM</sub>	Repetitive peak reverse voltage			30	V	
I <sub>F(RMS)</sub>	Forward rms current			10	Α	
1	Average forward current $\delta = 0.5$	T <sub>c</sub> = 140 °C	Per diode	7.5	Α	
I <sub>F(AV)</sub>	Average forward current $\delta = 0.5$		Per device	15	^	
I <sub>FSM</sub>	Surge non repetitive forward current	$t_p = 10 \text{ ms sin}$	nusoidal	75	Α	
I <sub>RRM</sub>	Peak repetitive reverse current	t <sub>p</sub> = 2 μs squa	are F= 1 kHz	1	Α	
P <sub>ARM</sub>	Repetitive peak avalanche power $t_p = 1 \mu s$ $T_j = 25 °C$			2800	W	
T <sub>stg</sub>	Storage temperature range	-65 to + 175	°C			
T <sub>j</sub>	Maximum operating junction temperature (1)			150	°C	

 $<sup>\</sup>frac{1}{\mathsf{Rth}(j\text{-}a)}$  condition to avoid thermal runaway for a diode on its own heatsink

Table 3. Thermal resistance

Symbol	Parameter	Value	Unit	
D		Per diode	2.5	
R <sub>th(j-c)</sub>	Junction to case	Total	1.6	°C/W
R <sub>th(c)</sub>	Coupling	0.7		

When diodes 1 and 2 are used simultaneously:

 $\Delta T_j$ (diode 1) = P(diode1) x  $R_{th(j-c)}$ (per diode) + P(diode 2) x  $R_{th(c)}$ 

Table 4. Static electrical characteristics (per diode)

(100,000)						
Parameter	Test conditions		Min.	Тур.	Max.	Unit
Reverse leakage	T <sub>j</sub> = 25 °C	V - V	-	-	1	mA
current	T <sub>j</sub> = 125 °C	$v_R = v_{RRM}$	-	70	140	mA
/F <sup>(1)</sup> Forward voltage drop	T <sub>j</sub> = 25 °C	I <sub>F</sub> = 7.5 A	-	-	0.48	
	T <sub>j</sub> = 125 °C	I <sub>F</sub> = 7.5 A	-	0.34	0.39	V
	T <sub>j</sub> = 25 °C	I <sub>F</sub> = 15 A	-	-	0.57	V
	T <sub>j</sub> = 125 °C	I <sub>F</sub> = 15 A	-	0.44	0.51	
	Reverse leakage current	Reverse leakage current $T_{j} = 25  ^{\circ}\text{C}$ $T_{j} = 125  ^{\circ}\text{C}$ $T_{j} = 25  ^{\circ}\text{C}$ $T_{j} = 25  ^{\circ}\text{C}$ $T_{j} = 125  ^{\circ}\text{C}$ $T_{j} = 25  ^{\circ}\text{C}$	$ \begin{array}{c c} \text{Reverse leakage} & T_j = 25  ^{\circ}\text{C} \\ \text{Current} & T_j = 125  ^{\circ}\text{C} \end{array} \\ \hline \\ Forward voltage drop & T_j = 25  ^{\circ}\text{C} & I_F = 7.5  \text{A} \\ \hline \\ T_j = 125  ^{\circ}\text{C} & I_F = 7.5  \text{A} \\ \hline \\ T_j = 25  ^{\circ}\text{C} & I_F = 15  \text{A} \end{array} $	$ \begin{array}{c ccccccccccccccccccccccccccccccccccc$	$ \begin{array}{c ccccccccccccccccccccccccccccccccccc$	$ \begin{array}{c ccccccccccccccccccccccccccccccccccc$

<sup>1.</sup> Pulse test:  $t_p$  = 380  $\mu$ s,  $\delta$  < 2%

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To evaluate the conduction losses use the following equation: P = 0.27 x  $I_{F(AV)}$  + 0.016  $I_{F}^{2}_{(RMS)}$ 

$$P = 0.27 \text{ x } I_{F(AV)} + 0.016 I_{F^2(RMS)}$$

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STPS15L30CDJF Characteristics

Figure 1. Average forward power dissipation Figure 2. Average forward current versus awerage forward current ambient temperature (per diode) ( $\delta$  = 0.5, per diode)

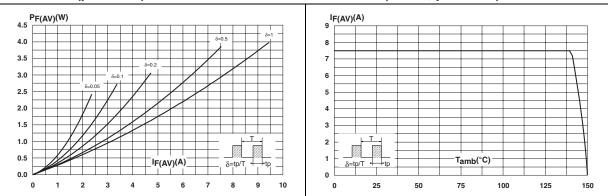


Figure 3. Normalized avalanche power derating versus pulse duration

Figure 4. Normalized avalanche power derating versus junction temperature

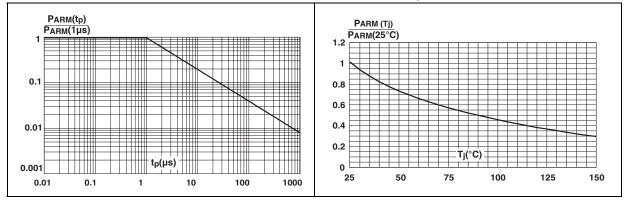
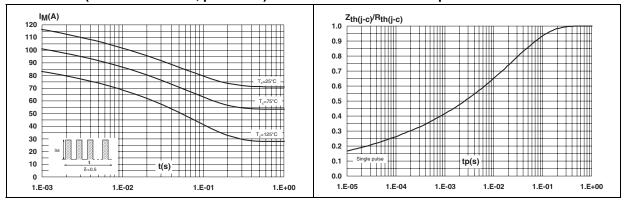


Figure 5. Non repetitive surge peak forward current versus overload duration (maximum values, per diode)

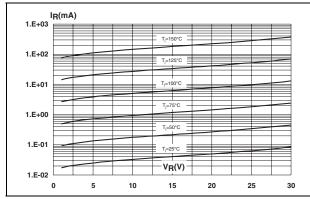
Figure 6. Relative variation of thermal impedance, junction to case, versus pulse duration



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Figure 7. Reverse leakage current versus reverse voltage applied (typical values, per diode)

Figure 8. Junction capacitance versus reverse voltage applied (typical values, per diode)



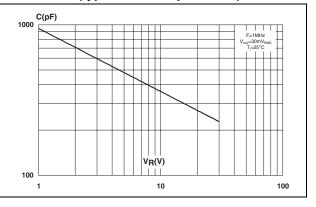
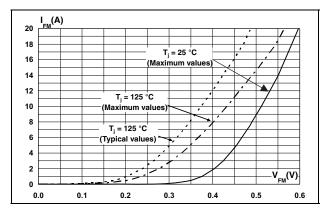
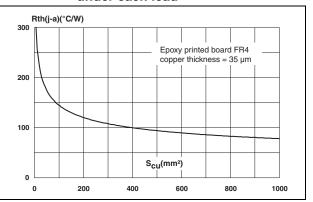


Figure 9. Forward voltage drop versus forward current (per diode)

Figure 10. Thermal resistance junction to ambient versus copper surface under each lead





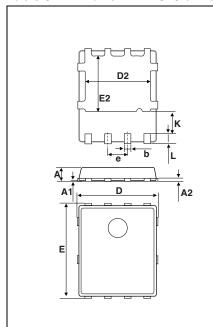
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### 2 Package information

- Epoxy meets UL94,V0
- Lead-free package

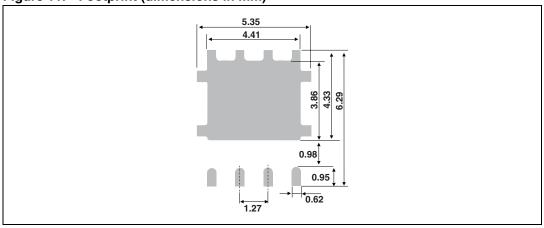
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Table 5. PowerFLAT 5x6 dimensions



	Dimensions					
Ref.	Millimeters		Inches			
	Min.	Тур.	Max.	Min.	Тур.	Max.
Α	0.80		1.00	0.031		0.039
A1	0.02		0.05	0.001		0.002
A2		0.25			0.010	
b	0.30		0.50	0.012		0.020
D		5.20			0.205	
D2	4.11		4.31	0.162		0.170
е		1.27			0.050	
Е		6.15			0.242	
E2	3.50		3.70	0.138		0.146
L	0.50		0.80	0.020		0.031
K	1.275		1.575	0.050		0.062

Figure 11. Footprint (dimensions in mm)



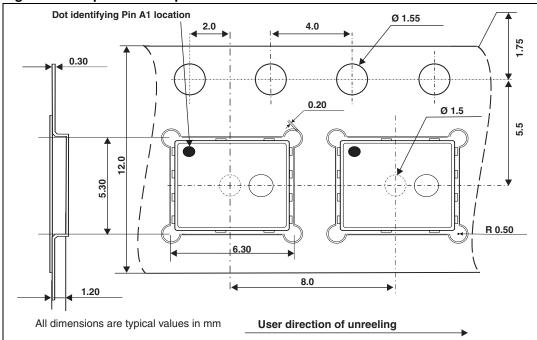


Figure 12. Tape and reel specifications

# 3 Ordering information

Table 6. Ordering information

Order code	Marking	Package	Weight	Base qty	Delivery mode
STPS15L30CDJFTR	PS15 L30C	PowerFLAT 5x6	0.095 g	3000	Tape and reel

## 4 Revision history

Table 7. Document revision history

Date	Revision	Changes
13-May-2009	1	First issue.
09-Nov-2009	2	Updated Table 1.
30-Jul-2010	3	Replace Power QFN with PowerFLAT. Updated Figure 9.
18-May-2011	4	Added reference E in <i>Table 5</i> . Updated package graphics. Removed dash from order code and updated marking in <i>Table 6</i> . Added <i>Figure 12</i> .

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