

Technical Note

Compact Video Driver Series for DSCs and Portable Devices



Compact, Low Current Consumption Single Output Video Drivers

BH76106HFV, BH76109HFV, BH76112HFV, BH76206HFV

No. 15064EBT03

Description

This video amplifier with built-in LPF uses a full output swing type output stage to make low voltage operation at Vcc = 2.6V possible.

In addition to advantages such as a tiny package and low power consumption, bands of the built-in LPF provide for 4.5 MHz products for DSC and other portable equipment and 6 MHz products for equipment such as DVD. Moreover, since it also can be used at Vcc = 5 V, it is suited not only to portable equipment but also to equipment for stationary use.

Features

- 1) Wide operating voltage range: Vcc = 2.6 V~5.5 V
- 2) Built-in 8th order LPF
- 3) Built-in sync-tip clamp circuit
- 4) Compact HVSOF6 package (3.0 mm × 1.6 mm × 0.75 mm)
- 5) Built-in standby function Standby current: 0 µA (typ.)
- 6) Selectable gain 6dB (BH76106HFV, BH76206HFV), 9dB (BH76109HFV), 12dB (BH76112HFV)
- 7) Selectable filter characteristics f = 4.5 MHz (BH761xxHFV), f = 6.0 MHz (BH76206HFV)

Applications

Mobile phone, DSC, DVC, DVD, and other

•Line up matrix

Product Name	Amplifier Gain (dB)	LPF Frequency (MHz)
BH76106HFV 6 4.5		4.5
BH76109HFV	9	4.5
BH76112HFV	12	4.5
BH76206HFV	6	6.0

Absolute Maximum Ratings

Parameter	Symbol	Rating	Unit
Power Supply Voltage	Vcc	7	V
Power Dissipation	Pd	410 *	mW
Operating Temperature Range	Topr	-40~+85	°C
Storage Temperature Range	Tstg	-55~+125	°C

* When mounted on a 70 mm × 70 mm × 1.6 mm ROHM standard board, reduce by 4.1mW/°C above Ta=+25°C

•Operating Range

Power Supply Voltage Vcc 2.6 3.0 5.5 V	Parameter	Symbol	Min.	Тур.	Max.	Unit
	Power Supply Voltage	Vcc	2.6	3.0	5.5	V

This product is not designed for protection against radio active rays.

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●Electrical Characteristics (Unless otherwise noted, Typ.: Ta = 25 °C, VCC = 3.0 V)

		Typical Values					
Parameter	Symbol	BH76106	BH76109	BH76112	BH76206	Unit	Measurement Conditions
		HFV	HFV	HFV	HFV		
Circuit Current 1	lcc1		7		8	mΑ	With no signal
Circuit Current 2	Icc2		0.	-	-	μA	In standby
Voltage Gain	Gv	6.0	9.0	12.0	6.0	dB	f=100kHz, Vin =1Vpp
Maximum Output Level	Vomv		2.	.6		Vpp	f=4.5MHz/100kHz(BH761xxHFV) f=6MHz/100kHz(BH76206HFV)
Frequency Characteristic 1	Gf1		0.1		-0.3	dB	f=4.5MHz/100kHz(BH761xxHFV) f=6MHz/100kHz(BH76206HFV)
Frequency Characteristic 2	Gf2		-4	.0		dB	f=8.2MHz/100kHz(BH761xxHFV) f=12MHz/100kHz(BH76206HFV)
Frequency Characteristic 3	Gf3		-45.0		-40.0	dB	f=19MHz/100kHz(BH761xxHFV) f=27MHz/100kHz(BH76206HFV)
Y Channel output S/N	SNY	-67.0				dB	100kHz~500kHz band 75 Ω termination 100% white video signal
C Channel output S/N (AM)	SNCA	-77.0				dB	100kHz~500kHz band 75 Ω termination 100% chroma video signal
C Channel output S/N (PM)	SNCP		-65.0		dB	100kHz~500kHz band 75 Ω termination 100% chroma video signal	
Differential Gain	DG		0.7		0.8	%	VIN = 1.0 V _{P-P} Standard stair-step signal
Differential Phase	Dp		0.7		0.8	deg	VIN = 1.0 V _{P-p} Standard stair-step signal
Standby Switching Voltage High Level	VthH	1.2~Vcc		V	Standby OFF		
Standby Switching Voltage Low Level	VthL	0~0.45		V	Standby ON		
Standby Switch Input Current High Level	lthH		45		66	μA	Applying 3.0 V to Pin 6

Control pin settings

Parameter	State	Function
	Н	Active
Standby (Pin 6)	L	Standby
	OPEN	Standby

Block Diagram



Pin Descriptions

(Typical voltage is that when $V_{CC} = 3.0 \text{ V}$, $T_a = 25 \text{ °C}$)

Pin No.	Pin Name	IN	OUT	Typical voltage	Equivalent Circuit	Function
4	Vcc	-	-	3.0V		Power supply pin

Pin No.	Pin Name	IN	OUT	Typical voltage	Equivalent Circuit	Function
1	GND	-	-	0V		GND pin
6	Stnby	0	-	-	(35k) ^{3%2} 45k ^{*1} (150k) ^{3%2} (150k) ^{3%2} (150k) ^{3%2}	Standby pin HIGH: Active LOW: Standby ※1 BH76106HFV BH76109HFV BH76112HFV ※2 BH76206HFV
5	Vin	0	-	1.4V		Video signal input pin This is a sync-tip clamp format video signal input pin. For the coupling capacitor, 0.1 µF is recommended.
2 3	Vsag Vout	-	0	0.2V	3Pin	Video signal output pin Video signal SAG correction pin

%The values show above (Voltage and resistance values) are reference values used for description, and are not guaranteed.

•Cautions on Use

- (1) Numeric values and data that are cited are representative design values and their values are not guaranteed.
- (2) Although we are confident recommending the application circuit example, carefully check the characteristics further in conjunction with its use. If using it after modifying externally attached component constants, try to determine adequate margins by including not just static characteristics but also transient characteristics to take into account variations in externally attached components and the ROHM LSI.
- (3) Absolute maximum ratings

If absolute maximum ratings such as applied voltage and operating temperature range are exceeded, the IC may be damaged. Do not apply voltages or temperatures that exceed the absolute maximum ratings. If you are considering circumstances in which an absolute maximum rating would be exceeded, implement physical safety measures such as fuses and investigate ways of not applying conditions exceeding absolute maximum ratings to the LSI.

(4) GND potential

Even if the voltage of the GND pin is left in an operating state, make it the minimum voltage. Actually confirm that the voltage of each pin does not become a lower voltage than the GND pin, including for transient phenomena.

(5) Thermal design

Perform thermal design in which there are adequate margins by taking into account the allowable dissipation under conditions of actual use.

- (6) Shorts between pins and mounting errors When mounting the LSI on a board, be careful of the direction of the LSI and of misalignment. If mounted badly and current is passed though it, the LSI may be damaged. The LSI also may be damaged if shorted by a foreign substance getting in between LSI pins, between a pin and the power supply, or between a pin and GND.
- (7) Operation in a strong electromagnetic field

Since the LSI could malfunction if used in a strong electromagnetic field, evaluate this carefully. (8) Input termination resistor

Since there is a risk of oscillation at low temperatures (approximately -60 °C) if the termination resistor of the input pin is made high impedance, set it to no more than 700 Ω .



If the termination resistor of the input pin is greater than 700 Ω , connect it as shown in the figure below.



(9) Standby pin

When the standby pin is open, the LSI is in a standby state.

Since adding a voltage greater than Vcc at the standby pin turns a protective diode ON, make this at most Vcc+0.2 V (no greater than Vcc+VF). (See Fig. 4) Applying a voltage to the standby pin when the voltage Vcc is not being applied also turns the protective diode ON, so do not apply a voltage.



%In relation to IC startup, this is practically 0 μs.Noise also does not occur when toggling the switch.

(10) Input coupling capacitor

Making the input coupling capacitor a value less than 0.1 μ F (the recommended value) increases SAG. Determine the capacitance of the input capacitor used after taking into consideration the relationship of SAG to input coupling capacitor.



Moreover, if you make the input coupling capacitor a value greater than 0.1 μ F (the recommended value), it may take time for the output waveform to stabilize. Decide the value of the coupling capacitor used by referring to the results shown in Fig. 10.



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BH76106/109/ 112/206HEV

- When input coupling capacitor (②) is 0.1 μF Time until output voltage stabilizes (③): 214 ms
- 2 . When input coupling capacitor (②) is 0.56 μF Time until output voltage stabilizes (③): 1.11 s
- 3. When input coupling capacitor (②) is 1 μF Time until output voltage stabilizes (③): 2.03 s

(11) SAG correction

In order to make the SAG of the video signal as small as possible, we recommend the values of the application circuit diagram for output coupling capacitor capacitance.

If reducing capacitance due to the demands of miniaturization or the like, check the SAG characteristic for an alternating black and white bounce signal *1, Hbar signal *2, or other signal for which a SAG effect readily occurs and use a capacitance that satisfies the demands of the set being used.

As a reference, try the combinations shown below when reducing capacitance. As the capacitance of the Vout capacitor is made smaller, SAG becomes greater.

*1,*2: TG-7 U705 unit or other

Vsag Capacitor (C1)	33 µ F	33 µ F	33 µ F
VOUT Capacitor (C2)	68 µ F	47 μ F	33 µ F

(12) Using after removing output coupling capacitor

An application circuit that is an example of use after removing the output coupling capacitor is shown in the figure below.



Fig.11

By eliminating the output coupling capacitor, not only can you reduce board space and product cost, but improvement of the SAG characteristic also can be realized due to the fact that the low-band frequency characteristic is improved. However, since direct current will flow in a set connected on the opposite side due to eliminating the output coupling capacitor, pay close attention to the specifications of what is connected in conjunction with using it. Moreover, characteristics such as circuit current, differential gain, and differential phase differ as shown below.

Parameter	With Output Coupling Capacitor	Without Output Coupling Capacitor
Circuit Current (If no signal)	7.1 mA	7.8 mA
Circuit Current (If color bar signal output)	8.3 mA	14.3 mA
Differential Gain (DG)	0.7%	1.0%
Differential Phase (DP)	0.7°	0.3°

The values shown above are reference values. They are not guaranteed values.

(13) Output dynamic range

The output dynamic range depends on the power supply voltage.

Be careful when using the LSI at low voltage.

The relationship of dynamic range to Vcc is shown in Fig. 19.

(14) Bypass capacitor

Since there is a risk of high frequency oscillation, position the power supply bypass capacitor as close as possible to the Vcc pin.

(15) Metal part of back of package

The metal part of the back of the package of this IC also serves as a heat sink. Since it is connected to the GND of the IC, when mounting the IC, connect it to GND or make it NC.

Moreover, since there is a risk of shorting, avoid passing a wire other than a GND under the IC.

(16) HVSOF6 Reference mounting pattern



Application Circuit Example



Reference Data

XValues shown below are reference values. They are not guaranteed values.







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JAPAN	USA	EU	CHINA
CLASSⅢ	CLASSⅢ	CLASS II b	
CLASSⅣ	CLASSII	CLASSⅢ	CLASSII

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 - [d] Use of our Products in places where the Products are exposed to static electricity or electromagnetic waves
 - [e] Use of our Products in proximity to heat-producing components, plastic cords, or other flammable items
 - [f] Sealing or coating our Products with resin or other coating materials
 - [g] Use of our Products without cleaning residue of flux (even if you use no-clean type fluxes, cleaning residue of flux is recommended); or Washing our Products by using water or water-soluble cleaning agents for cleaning residue after soldering
 - [h] Use of the Products in places subject to dew condensation
- 4. The Products are not subject to radiation-proof design.
- 5. Please verify and confirm characteristics of the final or mounted products in using the Products.
- 6. In particular, if a transient load (a large amount of load applied in a short period of time, such as pulse. is applied, confirmation of performance characteristics after on-board mounting is strongly recommended. Avoid applying power exceeding normal rated power; exceeding the power rating under steady-state loading condition may negatively affect product performance and reliability.
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For details, please refer to ROHM Mounting specification

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This Product is electrostatic sensitive product, which may be damaged due to electrostatic discharge. Please take proper caution in your manufacturing process and storage so that voltage exceeding the Products maximum rating will not be applied to Products. Please take special care under dry condition (e.g. Grounding of human body / equipment / solder iron, isolation from charged objects, setting of lonizer, friction prevention and temperature / humidity control).

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