



# 3A TrenchSBR TRENCH SUPER BARRIER RECTIFIER

### Product Summary (@ T<sub>A</sub> = +25°C)

V <sub>RRM</sub> (V)	I <sub>O</sub> (A)	V <sub>F(MAX)</sub> (V)	I <sub>R(MAX)</sub> (mA)
30	3	0.49	0.02

#### **Features and Benefits**

- Low Power Loss, High Efficiency
- Low Reverse Leakage Current
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)

# **Description and Applications**

Packaged in the compact U-DFN3030-8 package, the TrenchSBR SBRT3M30LP provides excellent low reverse leakage stability at high temperatures. It is ideal for use in low voltage, high frequency inverters, as well as freewheeling and polarity protection applications.

- AC-DC Adaptors/Chargers
- DC-DC Converters
- Bypass Diode
- Boost Diode
- Blocking Diode
- · Recirculating Diode

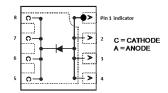
#### **Mechanical Data**

- Case: U-DFN3030-8
- Case Material: Molded Plastic, "Green" Molding Compound;
  UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish NiPdAu over Copper Leadframe; Solderable per MIL-STD-202, Method 208@4
- Polarity: See Diagram
- Weight: 0.0172 grams (Approximate)

#### U-DFN3030-8



**Bottom View** 



Top View Schematic and Pin Configuration

## **Ordering Information** (Note 4)

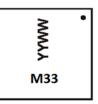
- 7				
	Part Number	Qualification	Case	Packaging
	SBRT3M30LP-7	Commercial	U-DFN3030-8	3,000/Tape & Reel

Notes:

- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.
- 2. See http://www.diodes.com/quality/lead\_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. For packaging details, go to our website at http://www.diodes.com/products/packages.html.

# **Marking Information**

U-DFN3030-8



M33 = Product Marking Code YYWW = Date Code Marking YY = Last Two Digits of Year (ex: 16 for 2016) WW = Week Code (01 to 53)



## **Maximum Ratings** (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Single phase, half wave, 60Hz, resistive or inductive load.

For capacitive load, derate current by 20%.

Characteristic	Symbol	Value	Unit
Peak Repetitive Reverse Voltage Working Peak Reverse Voltage DC Blocking Voltage	Vrrm V <sub>rwm</sub> V <sub>r</sub>	30	>
RMS Reverse Voltage	V <sub>R(RMS)</sub>	21	V
Average Rectified Output Current	lo	3.0	Α
Non-Repetitive Peak Forward Surge Current 8.3ms Single Half Sine-Wave Superimposed on Rated Load	I <sub>FSM</sub>	30	Α

## **Thermal Characteristics**

Characteristic		Symbol	Value	Unit
Typical Thermal Resistance Junction to Case	(Note 5)	$R_{ heta JC}$	15	°C/W
Typical Thermal Resistance Junction to Ambient Air	(Note 5)	$R_{\theta JA}$	152	°C/W
Typical Thermal Resistance Junction to Case	(Note 6)	$R_{\theta JC}$	7	°C/W
Typical Thermal Resistance Junction to Ambient Air	(Note 6)	$R_{ heta JA}$	76	°C/W
Operating and Storage Temperature Range		T <sub>J</sub> , T <sub>STG</sub>	-65 to +175	°C

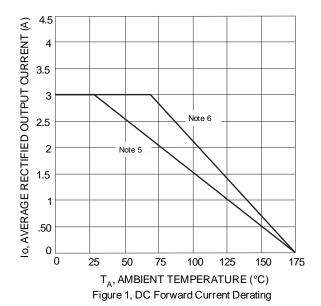
# Electrical Characteristics (@T<sub>A</sub> = +25°C, unless otherwise specified.)

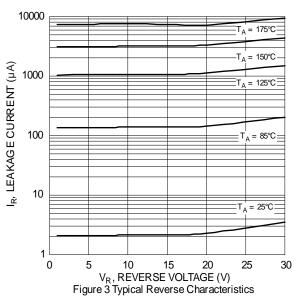
Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
Reverse Breakdown Voltage (Note 7)	$V_{(BR)R}$	30			V	$I_R = 0.02 \text{mA}$
Forward Voltage	V <sub>F</sub>	_	0.45	0.49	V	I <sub>F</sub> = 3.0A, T <sub>A</sub> = +25°C I <sub>F</sub> = 3.0A, T <sub>A</sub> = +125°C
orward voltage	VF		0.43			$I_F = 3.0A, T_A = +125$ °C
Reverse Current (Note 7)	I <sub>R</sub>	_	0.005	0.02	mA	$T_J = +25^{\circ}C, V_R = 30V$
Total Capacitance	Ст	_	100	_	pF	$f = 1MHz, V_R = 30V$
Reverse Recovery Time	t <sub>RR</sub>		16			I <sub>F</sub> =0.5A, I <sub>R</sub> =1.0A, I <sub>RR</sub> =0.25A

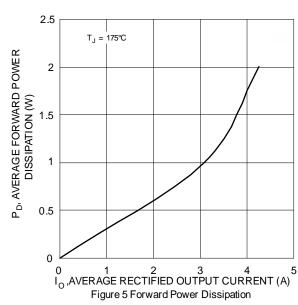
Notes:

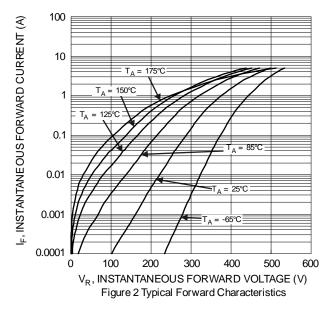
- 5. FR-4 PCB, 2 oz. Copper, minimum recommended pad layout per http://www.diodes.com/package-outlines.html.  $T_A = +25^{\circ}C$ .
- PCB with 1-inch sq. Copper pad, 2oz.
  Short duration pulse test used to minimize self-heating effect.

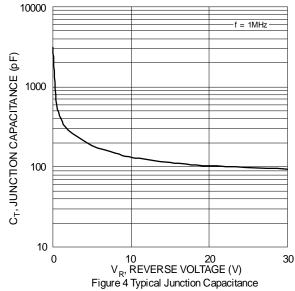


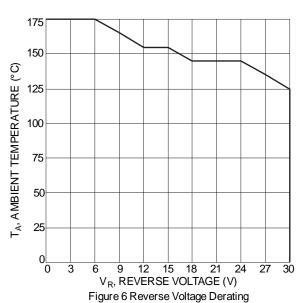










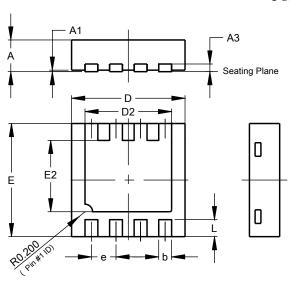




# **Package Outline Dimensions**

Please see http://www.diodes.com/package-outlines.html for the latest version.

#### U-DFN3030-8

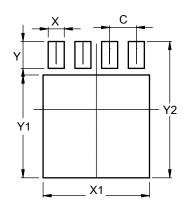


U-DFN3030-8					
Dim	Min	Max	Тур		
Α	0.57	0.63	0.60		
A1	0	0.05	0.02		
A3	ı	-	0.15		
b	0.29	0.39	0.34		
D	2.90	3.10	3.00		
D2	2.19	2.39	2.29		
е	-	-	0.65		
Е	2.90	3.10	3.00		
E2	1.64	1.84	1.74		
L	0.30	0.60	0.45		
All Dimensions in mm					

# **Suggested Pad Layout**

Please see http://www.diodes.com/package-outlines.html for the latest version.

#### U-DFN3030-8



Dimensions	Value		
Dillielisions	(in mm)		
С	0.650		
Х	0.390		
X1	2.590		
Υ	0.650		
Y1	2.490		
Y2	3.300		



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